



# SERVICE MANUAL

PMR446 TRANSCEIVER

## **IC-F27SR**

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S-14809XZ-C1  
Oct. 2011

Icom Inc.

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## INTRODUCTION

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This service manual describes the latest technical information for the **IC-F27SR** PMR446 TRANSCEIVER, at the time of publication.

MODEL	VERSION	FREQUENCY RANGE	CHANNEL SPACING
IC-F27SR	EUR-22	446.00625– 446.09375 MHz	12.5 kHz
	EUR-23		
	UK-02		
	UK-03		

To upgrade quality, any electrical or mechanical parts and internal circuits are subject to change without notice or obligation.

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## CAUTION

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**NEVER** connect the transceiver to an AC outlet or to a DC power supply that uses more than the specified voltage. This will ruin the transceiver.

**DO NOT** expose the transceiver to rain, snow or any liquids.

**DO NOT** reverse the polarities of the power supply when connecting the transceiver.

**DO NOT** apply an RF signal of more than 20 dBm (100 mW) to the antenna connector. This could damage the transceiver's front-end.

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## ORDERING PARTS

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Be sure to include the following four points when ordering replacement parts:

1. 10-digit Icom part number
2. Component name
3. Equipment model name and unit name
4. Quantity required

### <ORDER EXAMPLE>

1110003491 S.IC TA31136FNG IC-F27SR MAIN UNIT 5 pieces  
8820001210 Screw 2438 screw IC-F27SR Top cover 10 pieces

Addresses are provided on the inside back cover for your convenience.



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## REPAIR NOTES

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1. Make sure that the problem is internal before disassembling the transceiver.
2. **DO NOT** open the transceiver until the transceiver is disconnected from its power source.
3. **DO NOT** force any of the variable components. Turn them slowly and smoothly.
4. **DO NOT** short any circuits or electronic parts. An insulated tuning tool **MUST** be used for all adjustments.
5. **DO NOT** keep power ON for a long time when the transceiver is defective.
6. **DO NOT** transmit power into a Standard Signal Generator or a Sweep Generator.
7. **ALWAYS** connect a 30 dB to 40 dB attenuator between the transceiver and a Deviation Meter or Spectrum Analyzer, when using such test equipment.
8. **READ** the instructions of the test equipment thoroughly before connecting it to the transceiver.

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**GENERAL**

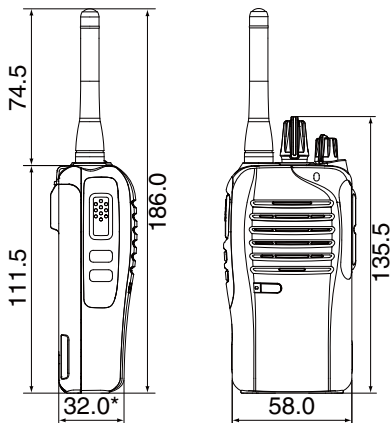
- Frequency coverage : 446.00625–446.09375 MHz
- Mode : 8K50F3E (FM)
- Current drain (at 7.2 V) : TX 400 mA (approximately)  
RX Max. audio 330 mA (approximately)
- Power supply requirement : 7.2 V DC nominal\* (negative ground)  
\*Only specified Icom's battery pack
- Frequency stability :  $\pm 2.5$  ppm ( $-25^{\circ}\text{C}$  to  $+55^{\circ}\text{C}$ )
- Antenna impedance :  $50\ \Omega$  nominal
- Weight (approximately) : 340 g (with BP-264)  
285 g (with BP-265)

**TRANSMITTER**

- Output power : 0.5 W ERP
- Modulation system : Variable reactance frequency modulation
- Max. frequency deviation :  $\pm 2.5$  kHz
- Spurious emissions : 0.25  $\mu\text{W}$  (1 GHz or below)  
1.00  $\mu\text{W}$  (above 1 GHz)
- Adjacent channel power : 60 dB
- External mic. connector : 3 conductor 2.5 (d) mm/2.2 k $\Omega$

**RECEIVER**

- Receive system : Double conversion  
superheterodyne
- Sensitivity (20 dB SINAD) : 26.5 dB $\mu\text{V}/\text{m}$
- Squelch sensitivity : 26.5 dB $\mu\text{V}/\text{m}$  (threshold)
- Intermodulation rejection ratio : 86.29 dB $\mu\text{V}/\text{m}$
- Spurious response rejection ratio : 91.29 dB $\mu\text{V}/\text{m}$
- Adjacent channel selectivity : 81.29 dB $\mu\text{V}/\text{m}$
- Audio output power : 0.8 W (typical) at 5% distortion with a 12  $\Omega$  load  
0.4 W (typical) at 5% distortion with an 8  $\Omega$  load
- External speaker connector : 2 conductor 3.5 (d) mm/8  $\Omega$

**DIMENSIONS**

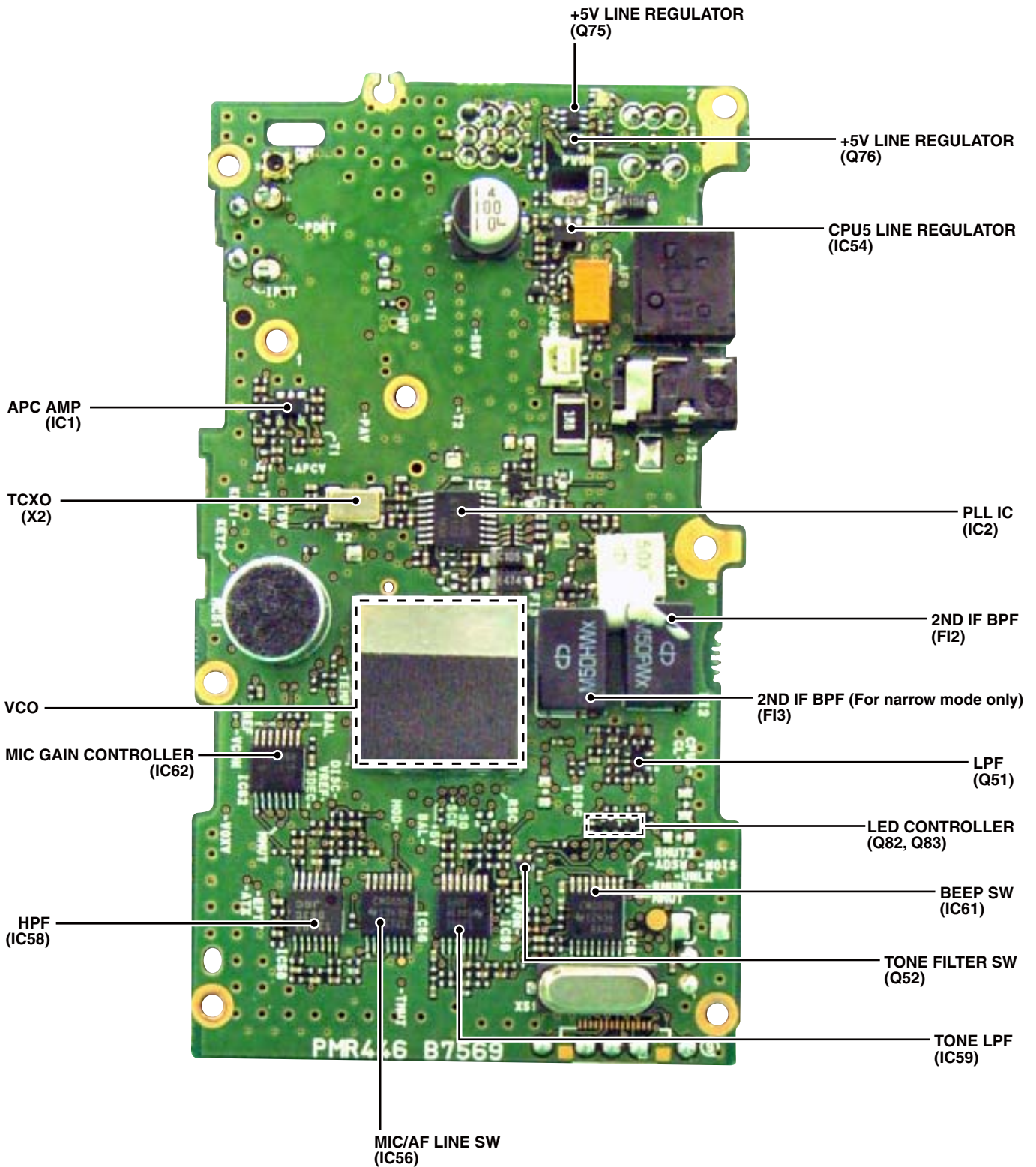
Unit: mm  
\*with a BP-264  
36.5 mm with a BP-265

Specifications are measured in accordance with EN 300 296.  
All stated specifications are subject to change without notice or obligation.

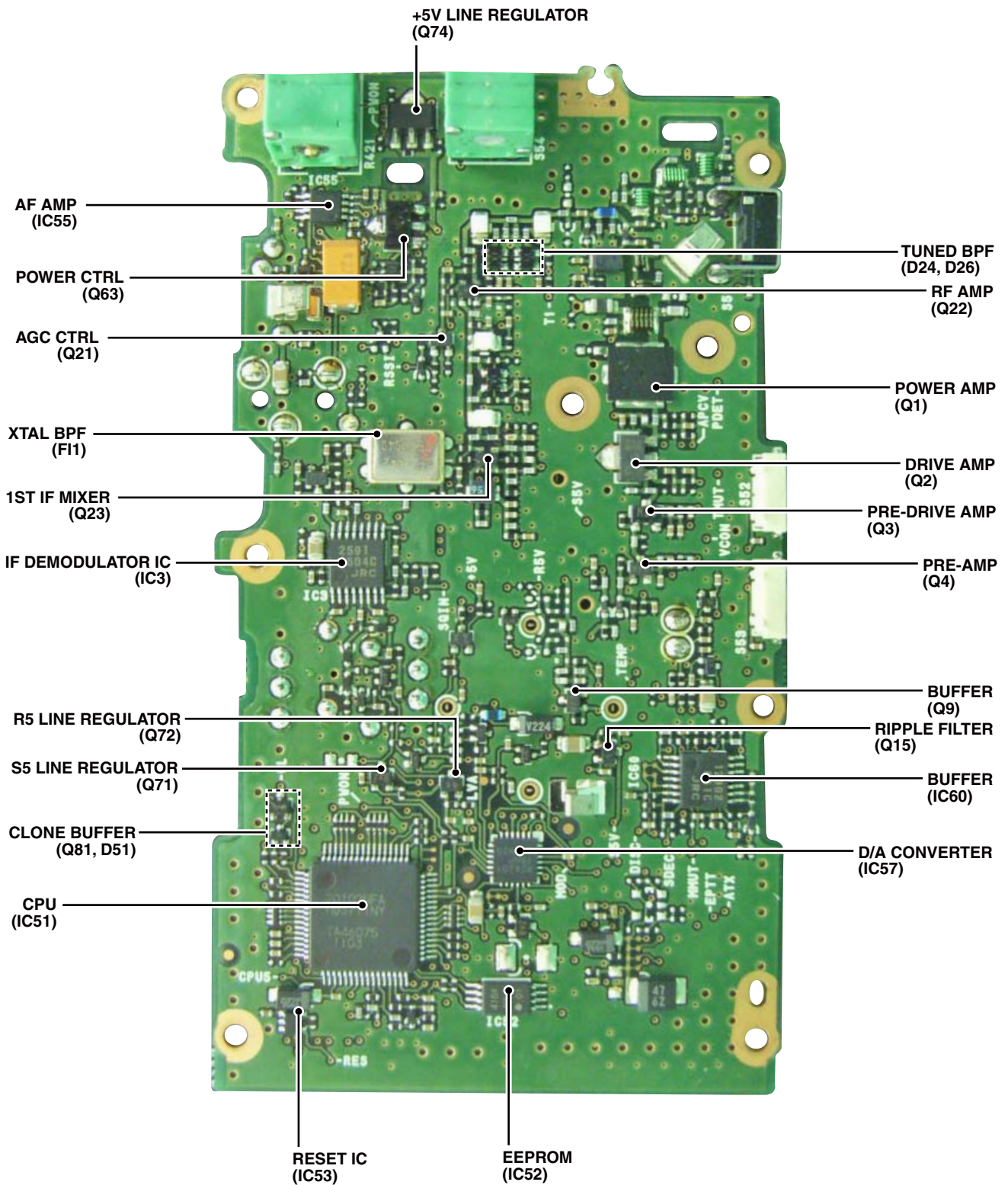
# SECTION 2

# INSIDE VIEWS

• MAIN-D UNIT  
(TOP VIEW)



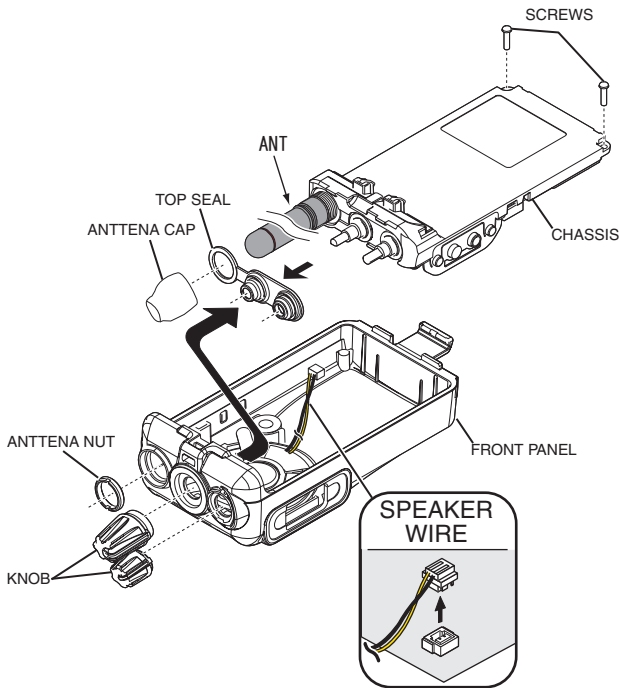
• MAIN-D UNIT  
(BOTTOM VIEW)



# SECTION 3 DISASSEMBLY INSTRUCTION

## 1. REMOVING THE CHASSIS

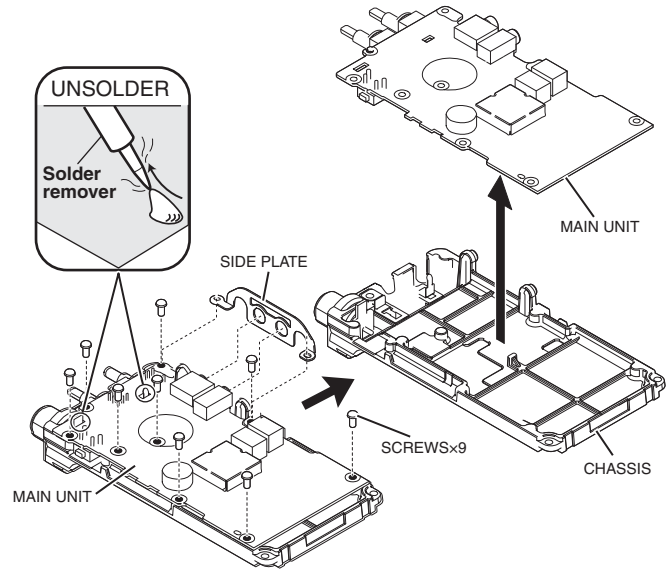
- 1) Remove the antenna cap, antenna nut, top seal and 2 knobs from the front panel.
- 2) Remove 2 screws from the chassis.
- 3) Unplug the speaker connector from the MAIN UNIT (J53).
- 4) Take off the chassis in the direction of the arrow.



**BE CAREFUL** to not pull out the **speaker wire** when separating the CHASSIS and the FRONT PANEL.

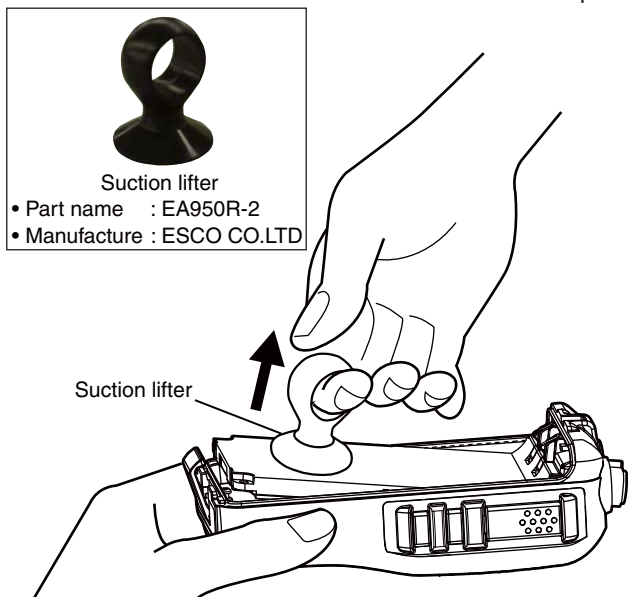
## 2. REMOVING THE MAIN UNIT

- 1) Remove the 9 screws and the side plate from the MAIN UNIT.
- 2) Unsolder the 2 points shown, and then remove the MAIN UNIT from the chassis.



### For easy separation of the CHASSIS

Use a suction lifter to lift the bottom of the CHASSIS up.



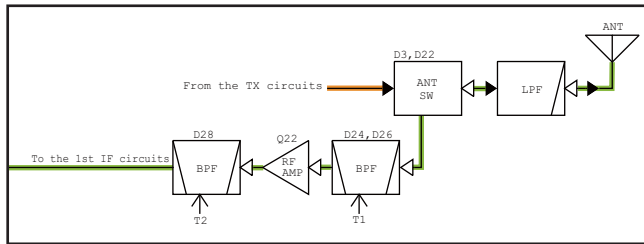
## 4-1 RECEIVER CIRCUITS

### RF CIRCUITS

The RX signal from the antenna is passed through the LPF and antenna SW, then filtered by the 2-staged tuned BPF to eliminate unwanted out-of-band signal. The filtered RX signal is amplified by the RF AMP, and filtered by another 2-staged tuned BPF to obtain a good image response, then applied to the 1st IF circuits.

The tuned BPFs are tuned to the RX frequency by applying adequate tuning voltages; "T1" and "T2" to the variable capacitors.

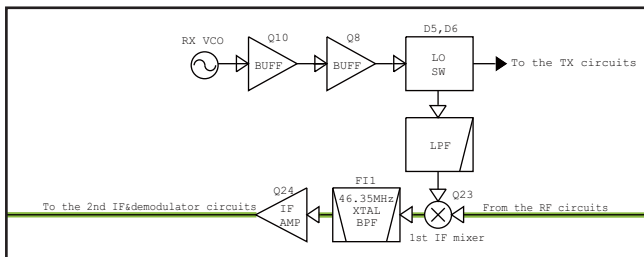
### • RF CIRCUITS



### 1ST IF CIRCUITS

The RX signal from the RF circuits is applied to the 1st IF mixer and mixed with the 1st LO signal from the RX VCO, resulting in the 46.35 MHz 1st IF signal. The 1st IF signal is filtered by the crystal filter, amplified by the 1st IF AMP, then applied to the 2nd IF circuits.

### • 1ST IF CIRCUITS



### 2ND IF CIRCUITS AND DEMODULATOR

The signal from the 1st IF circuits is applied to the IF demodulator IC which contains the 2nd IF mixer, 2nd IF AMP, FM detector, etc. in its package.

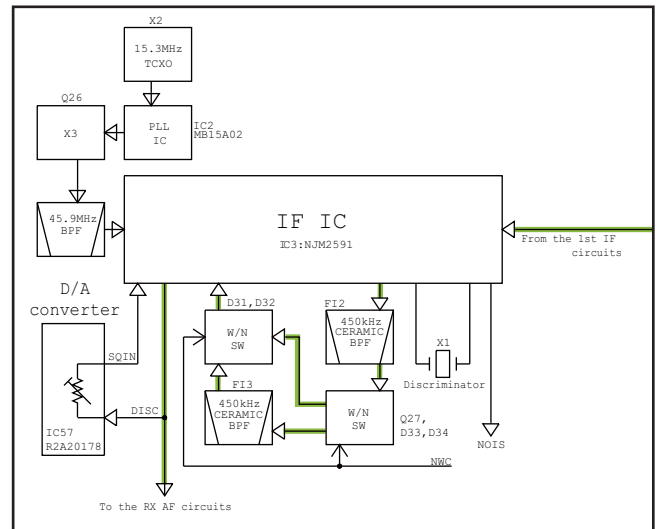
The 1st IF signal is applied to the 2nd IF mixer and mixed with the 2nd LO signal resulting in the 450 kHz 2nd IF signal.

The 2nd LO signal is generated by tripling the 15.3 MHz reference frequency signal oscillated by the reference frequency oscillator (TCXO).

The converted 2nd IF signal is filtered by the crystal filter/filters to eliminate unwanted signal, and amplified by the 2nd IF AMP, then demodulated by the detector circuit which employs a discriminator as the phase shifter.

The demodulated AF signal is applied to the RX AF circuits.

### • 2ND IF CIRCUITS



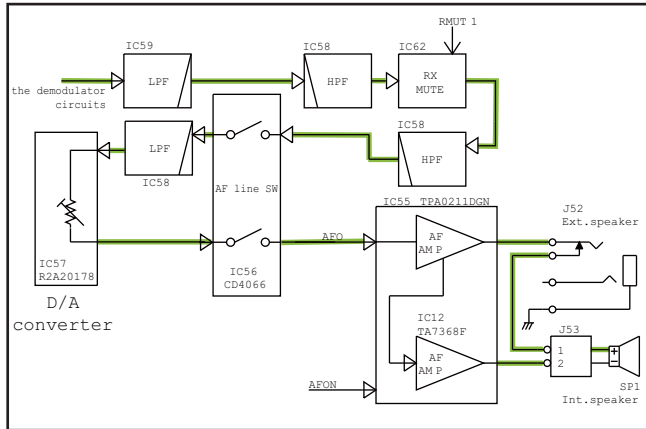


**RX AF CIRCUITS**

The demodulated AF signal from the IF demodulator IC is passed through the LPF, HPF which attenuates frequencies 300 Hz and below. The filtered AF signal is passed through the AF line SW, HPF which additionally attenuates frequencies 300 Hz and below, de-emphasis circuit which provides -6 dB/oct of frequency response. The de-emphasized AF signal is passed through the AF SW, LPF which attenuates frequencies 3 kHz and above, is adjusted in level by the D/A converter. The level-adjusted AF signal is then applied to the AF power AMP via the AF line SW.

The amplified AF signal is applied to the internal or external speaker.

**• RX AF CIRCUITS**



**4-2 TRANSMITTER CIRCUITS**

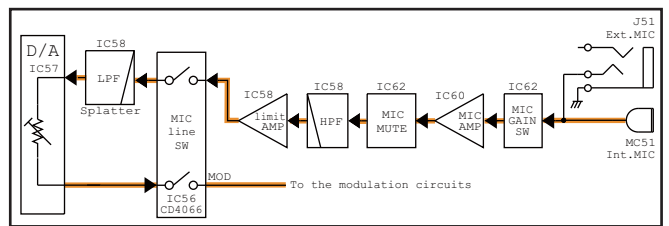
**TX AF CIRCUITS**

The audio signal from the internal or external microphone (MIC signal) is passed through the MIC gain SW and applied to the MIC AMP. The amplified MIC signal is passed through the MIC mute SW, HPF which attenuates frequencies 300 Hz and below. The filtered MIC signal is passed through the pre-emphasis circuit which provides +6 dB/oct of frequency response, then they applied to the limiter AMP.

The amplitude-limited MIC signal is passed through the MIC line SW, and applied to the splatter filter, which attenuate harmonics in the MIC signal to prevent exceeding permissible deviation.

The filtered MIC signal is applied to the D/A converter which adjusts its level (=deviation). The level-adjusted MIC signal is applied to the TX VCO as the modulation signal.

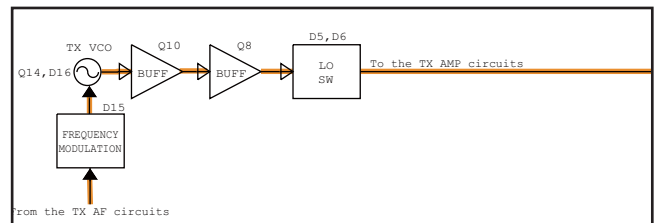
**• TX AF CIRCUITS**



**MODULATION CIRCUIT**

The modulation signal from the TX AF circuits is applied to D15 of the TX VCO to frequency-modulate it. The frequency-modulated signal from the TX VCO is buffer-amplified by two buffers, and applied to the TX AMP circuits via the TX/RX SW (D5).

**• MODULATION CIRCUITS**



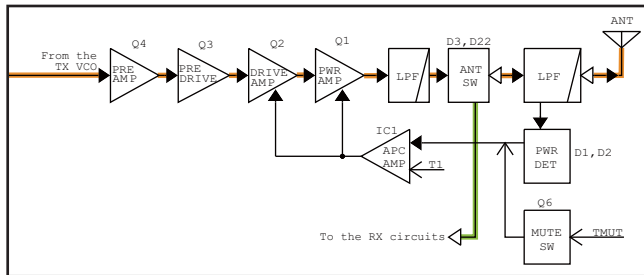
**TX AMPLIFIERS**

The buffer amplified signal from the LO SW (D5) is amplified by; the pre-AMP, pre-drive AMP, drive AMP, and power AMP in sequence, to obtain TX power. The amplified TX signal is passed through the antenna SW and the LPF, which eliminates harmonics, and then fed to the antenna.

**APC CIRCUITS**

D1 and D2 rectify a portion of the TX signal to direct current, and the APC AMP compares the voltage and the TX power control reference voltage, "T1." The resulting voltage controls the gain of the power and drive AMPs to keep the TX power constant.

**• TX AMPLIFIERS AND APC CIRCUIT**



**4-3 FREQUENCY SYNTHESIZER CIRCUITS**

The RX VCO is composed of Q13, D11 and D12. The VCO output signal is buffer-amplified by two buffers and applied to the 1st IF mixer, via the LO SW (D6) and the LPF.

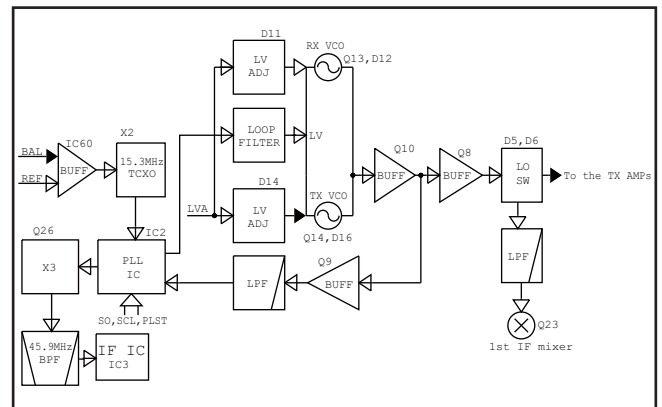
The TX VCO is composed of Q14 and D14–D16. The VCO output signal is buffer-amplified by two buffers and applied to the 1st IF mixer, via the LO SW (D6) and the LPF.

A portion of oscillated signal from each VCO is fed back to the PLL IC via the buffer and the LPF.

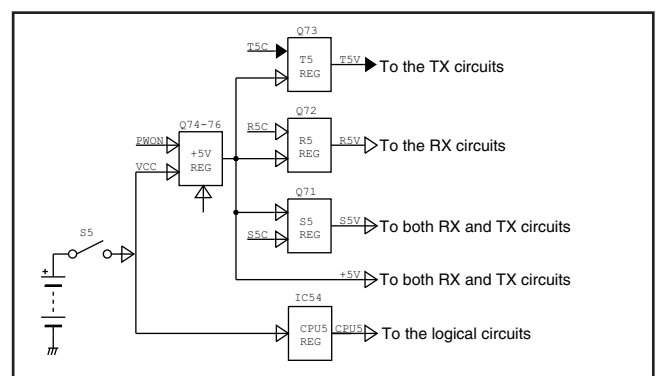
The applied VCO output signal is divided and phase-compared with a 15.3 MHz reference frequency signal from the TCXO, which is also divided. The resulting signal is output from the PLL IC, and DC-converted by the loop filter, and then applied to the VCO as the lock voltage.

When the oscillation frequency drifts, its phase changes from that of the reference frequency, causing a lock voltage change to compensate for the drift in the VCO oscillating frequency.

**• FREQUENCY SYNTHESIZER CIRCUITS**



**4-4 VOLTAGE BLOCK DIAGRAM**



## 4-5 PORT ALLOCATIONS

### • CPU (IC51)

PIN NO.	LINE NAME	DESCRIPTION	I/O
1	CDEC	Tone signal (CTCSS/DTCS) decoding input.	I
2	STONE	Tone signal (5/2 TONE, DTMF, BISS) decoding/encoding .	I/O
13	CSFT	Clock frequency shift. H=Clock frequency is shifted.	O
14	NWC	RX mode (narrow/wide) switching. L=While receiving in the narrow mode	O
15	EPTT	External PTT input. H=An external PTT is pushed.	I
16	ATX	Automatic TX control for VOX mode. H=Microphone input is detected.	O
17	BEEP	Beep sounds. (Square waves)	O
18	AFON	AF power AMP control. H=AF power AMP (IC55) is activated.	O
19	SCK	Common clock.	O
20	SO	Common serial data.	O
22	KEY2	[UPPER] key input. L=Pushed	I
23	KEY1	[LOWER] key input. L=Pushed	I
24	PLST	PLL strobe. H=Load enable.	I/O
25	DAST	D/A converter strobe. H=Load enable.	I/O
26	ESDA	EEPROM data.	I/O
27	ESCL	EEPROM clock.	I/O
28	R5C	Power supply switching control. H=Supplying current to the receiver circuits ("R5"line).	O
29	T5C	Power supply switching control. H=Supplying current to the transmitter circuits ("T5"line).	O
30	S5C	Power supply switching control. H=Supplying current to the RX/ TX common circuits ("S5"line).	O
31	PWON	Power supply switching control. H=The transceiver's power is ON.	O
32	IPTT	Internal PTT input. L=PTT pushed.	I
33, 34, 36	CENC2, CENC1, CENC0	CTCSS/DTCS signal.	O
37-40	CBI0-CBI3	[ROTARY SELECTOR] input.	I
41	DUSE	Frequency response switching. H=DTCS encoding	O
42	RLED	Busy LED (Green) control. L=LED lights. (Squelch open)	O
43	TLED	Busy LED (Red) control. L=LED lights. (Transmit)	O
47	MGC0	MIC gain control.	O
48	MGC1	MIC gain control.	O
49	CLI	Cloning data.	I
50	CLO		O

PIN NO.	LINE NAME	DESCRIPTION	I/O
51	ADSW	A/D conversion select. H=RSSI input, L=BATV input	O
53	NOIS	Noise level detect. H=Squelch close	I
54	UNLK	PLL unlock detect.	I
55	RMUT2	RX AF mute switch control. L=AF mute.	O
56	RMUT1	RX AF mute switch control. L=AF mute.	O
57	MMUT	MIC mute switch control. L=MIC mute.	O
58	TMUT	Transmission mute. L=TX inhibit.	O
59	MDET	External microphone connection detect.	I
60	LVIN	Lock voltage input.	I
61	TEMP	Temperature sensing voltage.	I
62	AFVI	[VOLUME CONTROL] input.	I
63	VOXV	Microphone input sensing voltage.	I
64	AD5	Remaining battery voltage. (or) RSSI voltage. (The input signal depends on the status of pin 51.)	I

### • D/A CONVERTER (IC57)

PIN No.	LINE NAME	DESCRIPTION
23	T1 (TXPO)	BPF tuning voltage TX power control
24	T2	BPF tuning voltage
7	REF	Reference frequency adjust voltage.
8	LVA	Additional lock voltage adjustment.
11	SQLC	Squelch threshold setting.
12	TENC	CTCSS deviation adjustment.
19	BAL	DTCS balance.
20	VRAF	Max. deviation/AF volume adjustment.







[MAIN-D UNIT]

REF NO.	PARTS NO.	DESCRIPTION	M.	H/V LOCATION
C381	4030016960	S.CER C1005 JB 1E 183K-T	B	68.4/8.0
C382	4030016960	S.CER C1005 JB 1E 183K-T	B	68.4/6.3
C383	4030017730	S.CER C1005 JB 1H 471K-T	B	70.2/4.6
C385	4030016930	S.CER C1005 JB 1A 104K-T	B	71.7/4.1
C388	4030017460	S.CER C1005 JB 1H 102K-T	B	20.9/3.5
C389	4030017460	S.CER C1005 JB 1H 102K-T	B	43.6/2.8
C390	4030017460	S.CER C1005 JB 1H 102K-T	B	53.1/2.8
C391	4030017420	S.CER C1005 CH 1H 470J-T	B	37.3/46.3
C392	4030017420	S.CER C1005 CH 1H 470J-T	B	29.5/43.9
C393	4520000020	S.NIO NOJC227M006RWJV	B	19.8/40.5
C395	4520000020	S.NIO NOJC227M006RWJV	T	21.7/37.1
C397	4030017460	S.CER C1005 JB 1H 102K-T	T	25.5/37.5
C398	4030017460	S.CER C1005 JB 1H 102K-T	B	12.2/32.0
C399	4030016930	S.CER C1005 JB 1A 104K-T	B	22.2/37.1
C400	4550006250	S.TAN TEESVA 1A 106M8R	T	12.9/40.8
C401	4030016930	S.CER C1005 JB 1A 104K-T	B	12.2/37.6
C403	4030016930	S.CER C1005 JB 1A 104K-T	B	15.0/39.5
C404	4030016930	S.CER C1005 JB 1A 104K-T	B	12.2/38.5
C405	4030018860	S.CER C1005 JB 0J 105K-T	B	19.3/33.7
C406	4030017460	S.CER C1005 JB 1H 102K-T	T	21.4/34.6
C408	4030016930	S.CER C1005 JB 1A 104K-T	T	5.2/37.8
C412	4030017460	S.CER C1005 JB 1H 102K-T	T	6.5/42.8
C413	4030017460	S.CER C1005 JB 1H 102K-T	T	6.8/44.8
C414	4030017460	S.CER C1005 JB 1H 102K-T	T	8.1/23.1
C415	4030017460	S.CER C1005 JB 1H 102K-T	T	6.5/30.8
C416	4030017460	S.CER C1005 JB 1H 102K-T	T	6.1/23.1
C417	4030017460	S.CER C1005 JB 1H 102K-T	T	3.7/30.8
C419	4030017420	S.CER C1005 CH 1H 470J-T	T	5.7/46.5
C421	4030016930	S.CER C1005 JB 1A 104K-T	T	75.4/12.0
C423	4030016790	S.CER C1005 JB 1E 103K-T	B	65.2/38.4
C424	4030017460	S.CER C1005 JB 1H 102K-T	T	63.8/34.4
C426	4030016790	S.CER C1005 JB 1E 103K-T	B	69.0/30.3
C427	4030016930	S.CER C1005 JB 1A 104K-T	T	73.7/26.0
C429	4030016790	S.CER C1005 JB 1E 103K-T	B	77.2/25.7
C430	4030016930	S.CER C1005 JB 1A 104K-T	T	65.9/11.4
C431	4510009870	S.ELE 10 CE 100 LD	T	14.7/27.1
C432	4030017460	S.CER C1005 JB 1H 102K-T	T	14.3/31.7
C433	4030016930	S.CER C1005 JB 1A 104K-T	T	16.2/32.2
C434	4030016790	S.CER C1005 JB 1E 103K-T	T	14.3/32.6
C435	4030017460	S.CER C1005 JB 1H 102K-T	T	18.0/32.2
C436	4550007320	S.TAN F930J226MAABMA	B	85.2/44.4
C437	4030016930	S.CER C1005 JB 1A 104K-T	T	17.1/32.2
C448	4030019490	S.CER C2012 JB 1A 106K-T	B	29.0/46.0
C451	4030017660	S.CER C1005 CH 1H 330J-T	B	86.6/38.5
C452	4030017620	S.CER C1005 CH 1H 100C-T	B	86.1/37.3
C453	4030017400	S.CER C1005 CH 1H 220J-T	B	87.0/35.2
C454	4030016930	S.CER C1005 JB 1A 104K-T	B	86.1/32.2
C456	4030016930	S.CER C1005 JB 1A 104K-T	B	87.9/32.2
C457	4030017030	S.CER C1005 JB 1A 273K-T	B	86.8/46.9
C458	4030016930	S.CER C1005 JB 1A 104K-T	T	84.7/45.5
C459	4030016930	S.CER C1005 JB 1A 104K-T	B	85.2/40.6
C460	4030016930	S.CER C1005 JB 1A 104K-T	B	79.5/46.1
C461	4030016930	S.CER C1005 JB 1A 104K-T	B	80.4/46.1
C462	4030016930	S.CER C1005 JB 1A 104K-T	B	81.3/46.1
C465	4030016930	S.CER C1005 JB 1A 104K-T	T	77.4/35.0
C466	4030016790	S.CER C1005 JB 1E 103K-T	T	79.0/35.0
C467	4030016930	S.CER C1005 JB 1A 104K-T	B	85.2/32.2
J1	6510028180	S.CON MM8130-2600RA2	T	9.3/7.1
J51	6450000131	CON HSJ1102-018540		
J52	6450002250	CON HSJ1456-010320		
J53	6510021901	S.CON BM02B-ASRS-TF(LF)(SN)	T	27.9/34.4
F51	5210001160	S.FUS ERBRE3R00V	T	10.7/37.6
DS51	5040002670	S.LED CL-165HR/YG	T	2.6/37.9
MC51	7700002920	MIC EM9745P-38-G-01 <HOR>		
S51	2260001900	SWI SW-149 (SKHLLD)		
S52	2260002800	S.SWI SW-167 (SKQTLAE010)	B	39.2/1.9
S53	2260002800	S.SWI SW-167 (SKQTLAE010)	B	48.7/1.9
S54	2250000680	ENC FSR080453W-01+16C L15.9 <SLVJ>		
EP3	6910018460	S.BEA MMZ1005Y102C-T	T	63.7/19.3
EP4	6910018460	S.BEA MMZ1005Y102C-T	T	41.9/19.4
EP5	6910018460	S.BEA MMZ1005Y102C-T	T	41.0/10.9
EP6	6910014730	S.BEA MPZ2012S331A-T	B	22.4/17.0
EP11	6910021240	S.BEA MMZ1005A152ET	T	54.9/24.5
EP14	6910021240	S.BEA MMZ1005A152ET	T	60.4/22.2
EP51	6910018460	S.BEA MMZ1005Y102C-T	B	19.1/46.2

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)  
S.=Surface mount

# SECTION 6

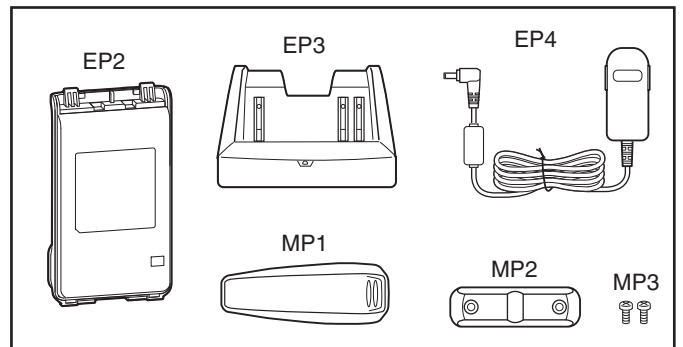
# MECHANICAL PARTS

## [CHASSIS PARTS]

REF NO.	ORDER NO.	DESCRIPTION	QTY.
SP1	2510001560	045P01202-02	1
W1	8900009640	OPC-963	1
EP2	3310003640	2927 ANT <EIK>	1
MP1	8010022041	3286 CHASSIS-1 2X2	1
MP2	8210026490	3328 FRONT PANELASSEMBLY	1
MP10	8930082160	3285 NAME SHEET (A)	1
MP11	8930081140	3328 LENS	1
MP12	8930080620	O-RING (CF) <KRI>	1
MP13	8930080630	3285 SP PLATE Y1174	1
MP14	8930042350	1922 MIC SHEET	1
MP15	8930080410	3285 MIC SPONGE	1
MP16	8930079900	3285 TERMINAL HOLDER	1
MP17	8930080100	3285 PLUS TERMINAL	1
MP18	8930080400	3285 MINUS TERMINAL	1
MP19	8930069710	THERMAL SHEET (BC) TC200HS-1.4 (7.9X7)	1
MP20	8830003390	VR NUT (AB)	2
MP21	8930080092	3285 SIDE PLATE-2 Y1173B	1
MP22	8930080150	3285 SIDE SEAL <KRI>	1
MP23	8930080140	3285 TOP SEAL <KRI>	1
MP25	8610014180	KNOB N-389	1
MP27	8610014190	KNOB N-390	1
MP31	8810008761	PHBT M2 X 8 NI-ZC3	2
MP32	8810009511	PHBT M2 X 4 NI-ZC3 (3.6-4.0)	9
MP33	8810009511	PHBT M2 X 4 NI-ZC3 (3.6-4.0)	1
MP34	8810009181	BT M2 X 5 NI-ZC3	2
MP35	8830002900	2927 ANT NUT <SSC>	1
MP36	6910018220	2927 ANT CAP <EIK>	1

## [ACCESSORIES]

REF NO.	ORDER NO.	DESCRIPTION	QTY.
EP2	(Optional)	BP-264 EXP [EUR-22]	1
	(Optional)	BP-265 EU [EUR-23]	1
	(Optional)	BP-264 EXP [UK-02]	1
	(Optional)	BP-265 EU [UK-03]	1
EP3	0880002840	EX-3289 #02 BC-192 EXP [EUR-22]	1
	0880002870	EX-3290 #02 BC-193 EX [EUR-23]	1
	0880002840	EX-3289 #02 BC-192 EXP [UK-02]	1
	0880002870	EX-3290 #02 BC-193 EX [UK-03]	1
EP4	(Optional)	BC-147SE [EUR-22]	1
	(Optional)	BC-123SE [EUR-23]	1
MP1	(Optional)	MB-124 (3297 CLIP)	1
MP2	8210025840	3285 JACK PANEL	1
MP3	8810004861	PH M2 X 6 ZK3	2



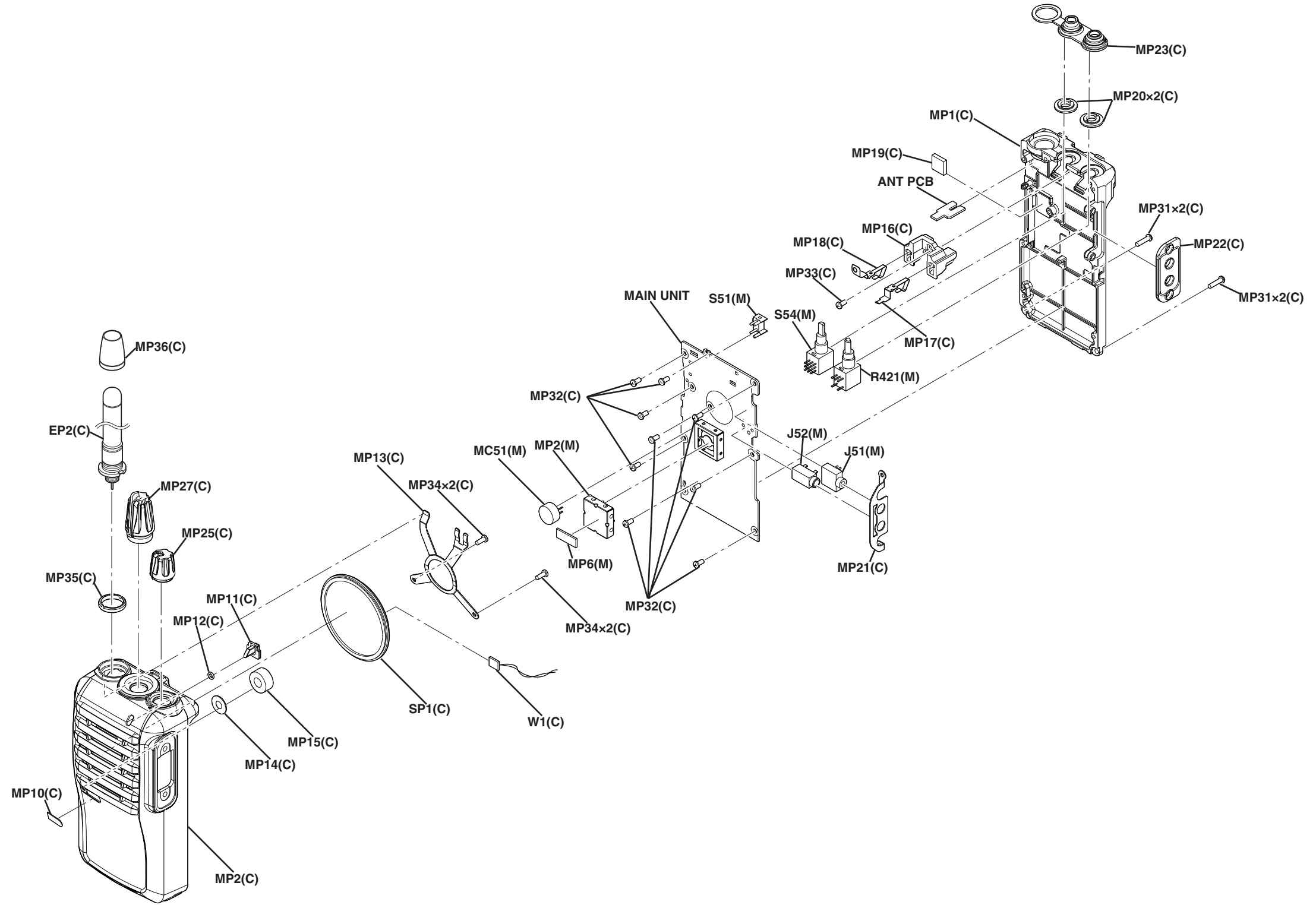
## [MAIN-D UNIT]

REF NO.	ORDER NO.	DESCRIPTION	QTY.
J1*	6510028180	MM8130-2600RA2	1
J51	6450000131	HSJ1102-018540	1
J52	6450002250	HSJ1456-010320	1
J53*	6510021901	BM02B-ASRS-TF (LF) (SN)	1
F51	5210001160	ERBRE3R00V	1
MC51	7700002920	EM9745P-38-G-01 <HOR>	1
S51	2260001900	SW-149 (SKHLLD)	1
S52*	2260002800	SW-167 (SKQTLAE010)	1
S53*	2260002800	SW-167 (SKQTLAE010)	1
S54	2250000680	FSR080453W-01+16C L15.9 <SLVJ>	1
MP2	8510016461	2775 VCO COVER-1	1
MP3*	8510016471	2775 VCO CASE-1	1
MP4*	6910014760	OG-503040	1
MP5*	6910014760	OG-503040	1
MP6	8930083030	SPONGE (LF)	1
MP8*	8510019750	3285 SHIELD PLATE Y1189	1
MP51*	6910014760	OG-503040	1

\*: Refer to "BOARD LAYOUTS" for the location.

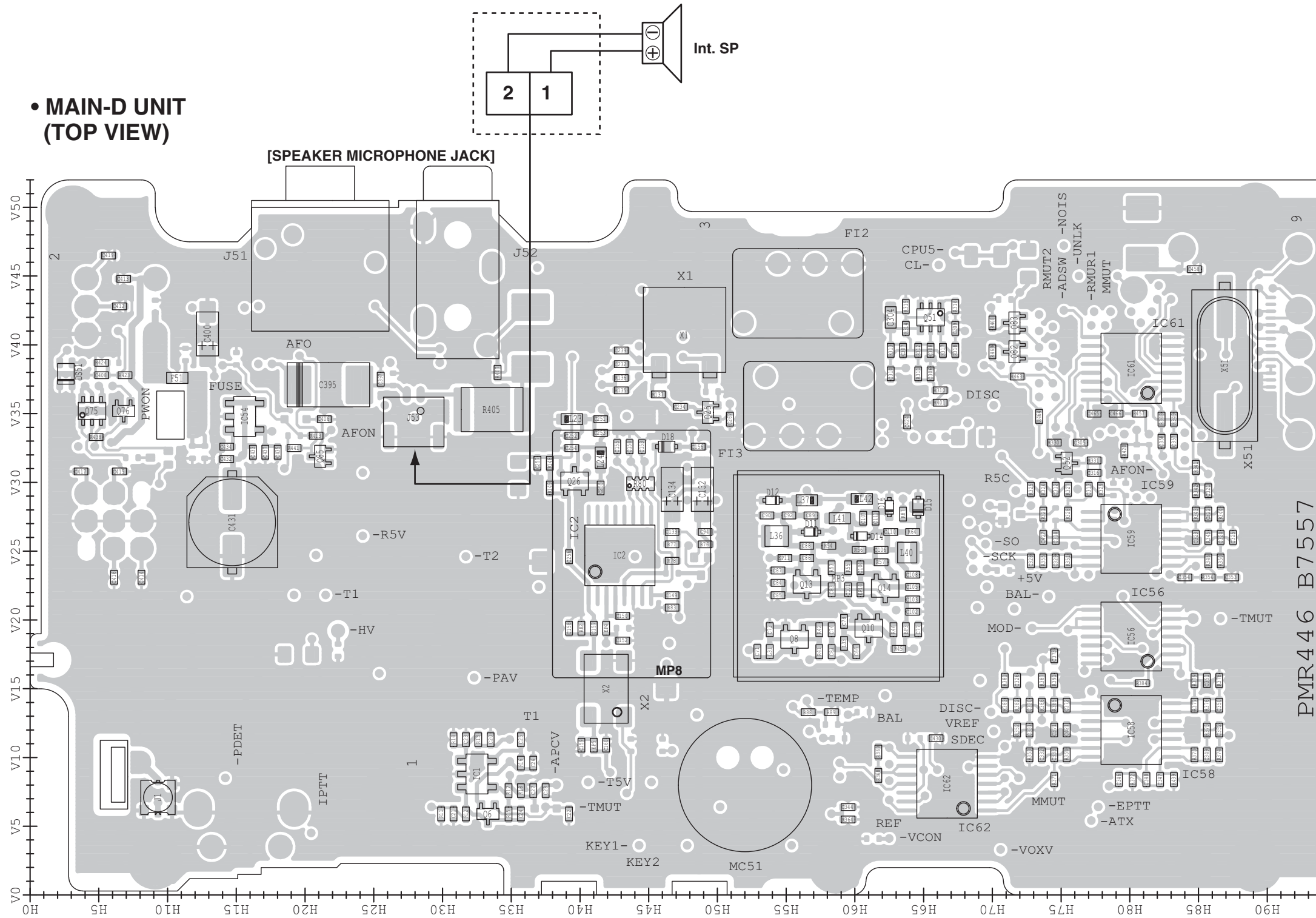
**Screw abbreviations** A, B0, BT: Self-tapping PH: Pan head ZK: Black NI-ZU: Nickel-Zinc SUS: Stainless





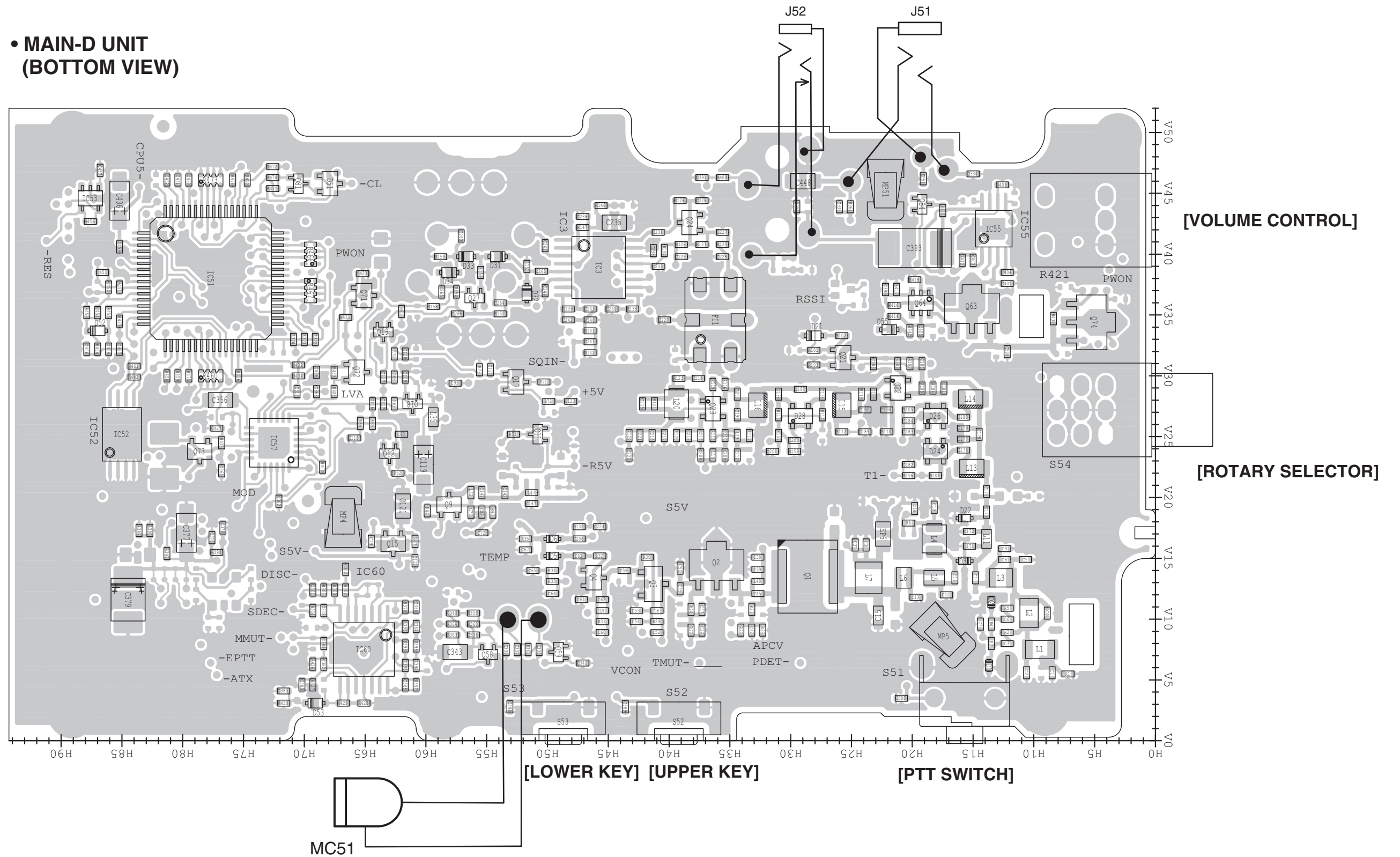
**UNIT ABBREVIATIONS**  
**(C): CHASSIS PARTS**  
**(M): MAIN-D UNIT**

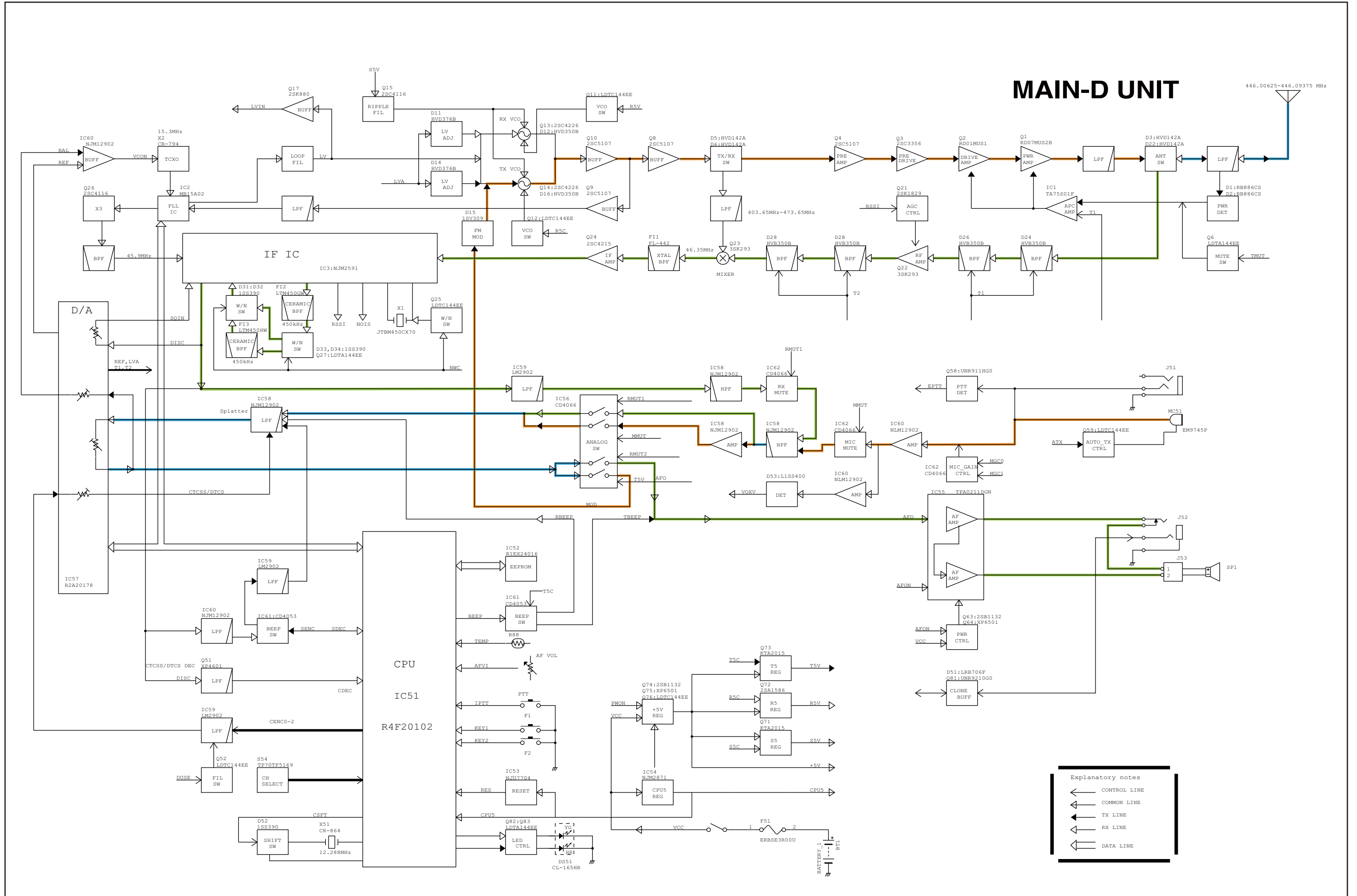
The combination of this page and next page shows the top side and bottom side of actual P.C. board.



The combination of this page and next page shows the top side and bottom side of actual P.C. board.

• MAIN-D UNIT  
(BOTTOM VIEW)

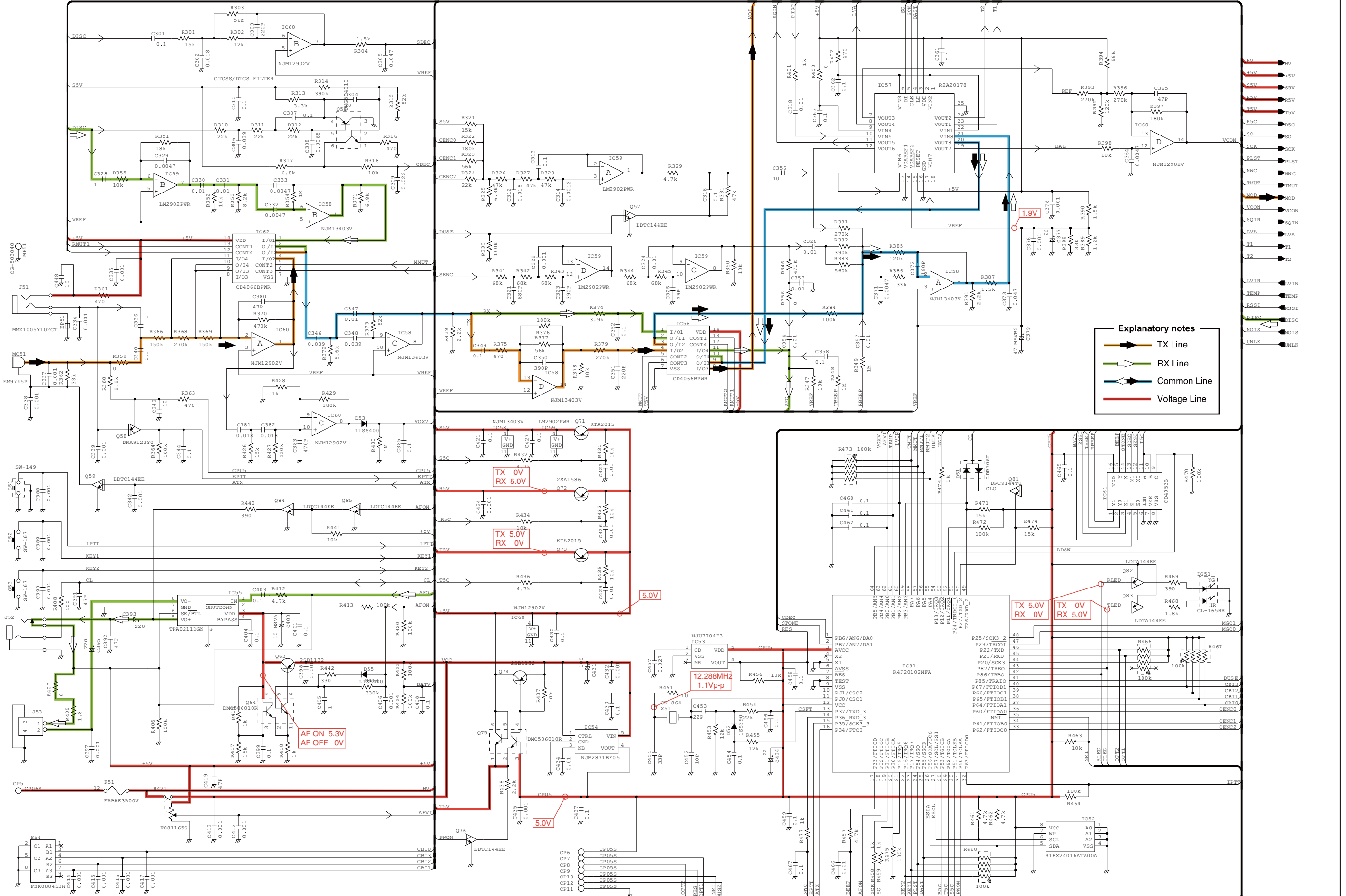




# SECTION 9

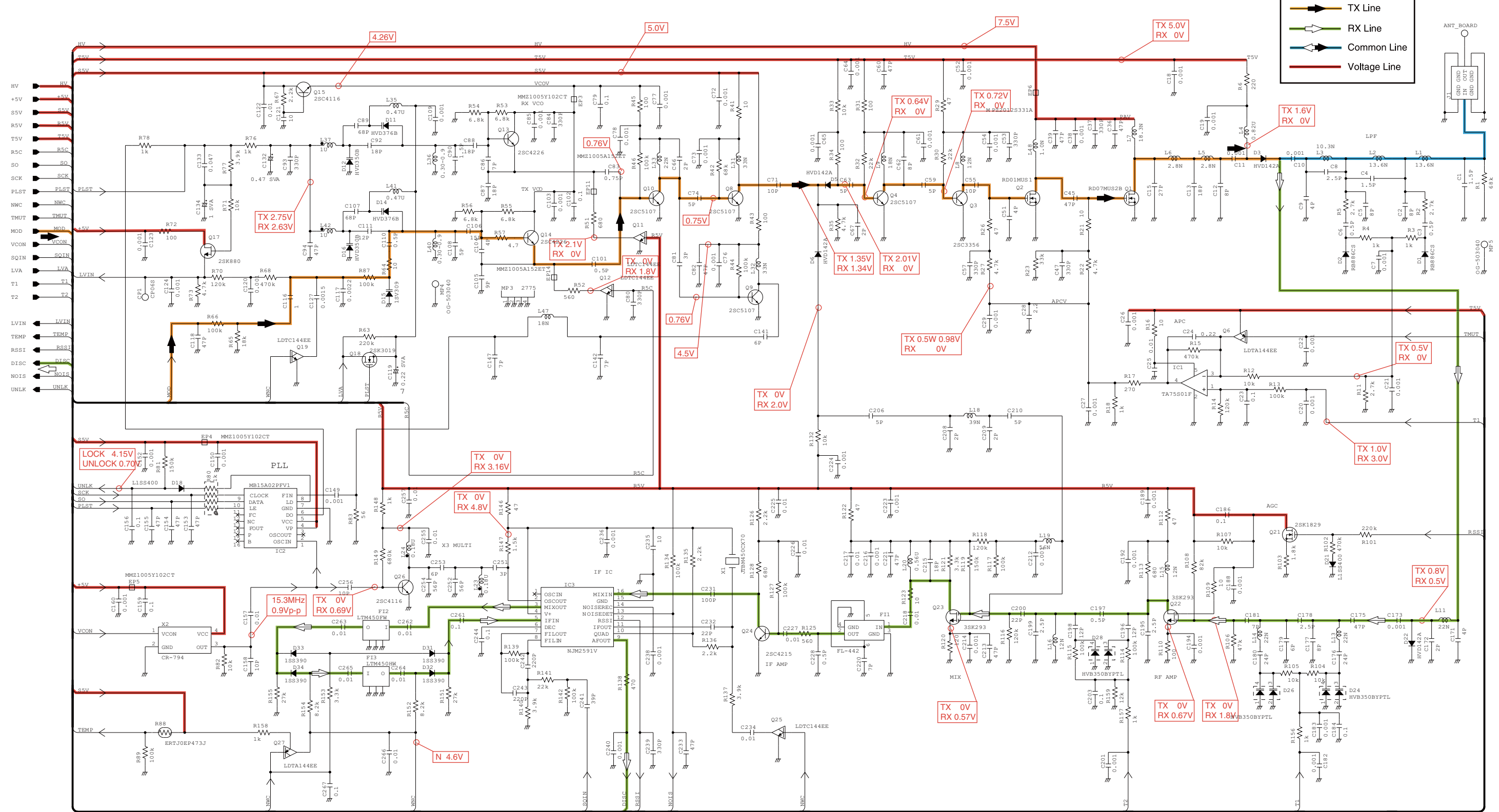
# VOLTAGE DIAGRAM

## • MAIN-D UNIT (1/2)



\*: Refer to the PARTS LIST for the value and name of component.

• MAIN-D UNIT (2/2)



**Explanatory notes**

- TX Line (Orange arrow)
- RX Line (Green arrow)
- Common Line (Blue arrow)
- Voltage Line (Red arrow)

\*: Refer to the PARTS LIST for the value and name of component.

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